SECTION 553-5011-507 Issued: 27 November 1974

Standard

# "PULSE\* 120" – SG-1A

## ELECTRONIC PRIVATE AUTOMATIC BRANCH EXCHANGE

# STATION LINE TRANSMISSION AND/OR DIALING FAULT

#### **CLEARING PROCEDURE**

#### 1. GENERAL

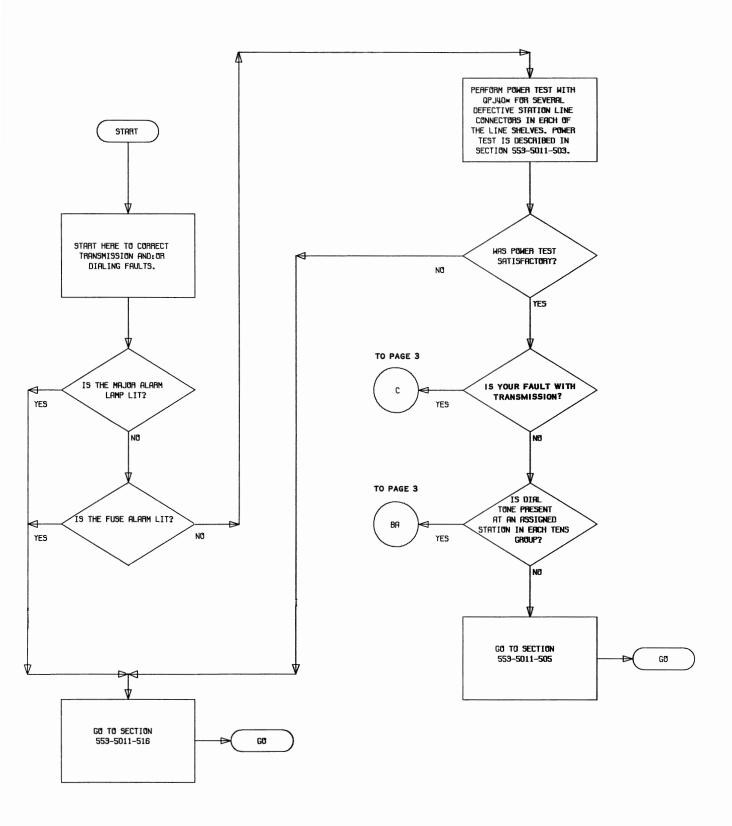
- 1.01 This section describes the procedure for clearing transmission and/or dialing faults.Transmission faults include noise, crosstalk, hum, and audible dial pulses at the faulty station lines in the PULSE 120 Electronic Private Automatic Branch Exchange (EPABX).
- 1.02 This section also describes the rotary dial fault clearing procedure. (For DIGITONE\* fault clearing procedures, see Section 553-5011-508.) The rotary dial faults usually occur in a single line, a 10's group, a complete shelf, or the whole system, and not in several lines in different 10's groups or line shelves.

## 2. FAULT CLEARING PROCEDURE

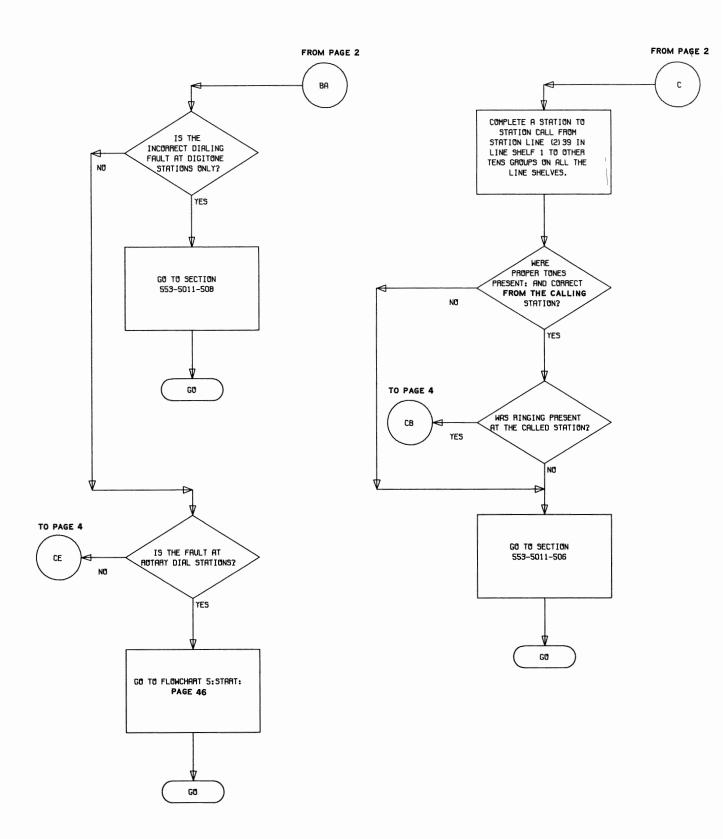
2.01 When the substitution of a circuit pack is required during the fault clearing procedure, the contacts on the new circuit pack must be cleaned as described in Section 553-5011-500 before inserting the circuit pack into the connector.

**Note:** This asterisk (\*) after the circuit pack code replaces the suffix letter.

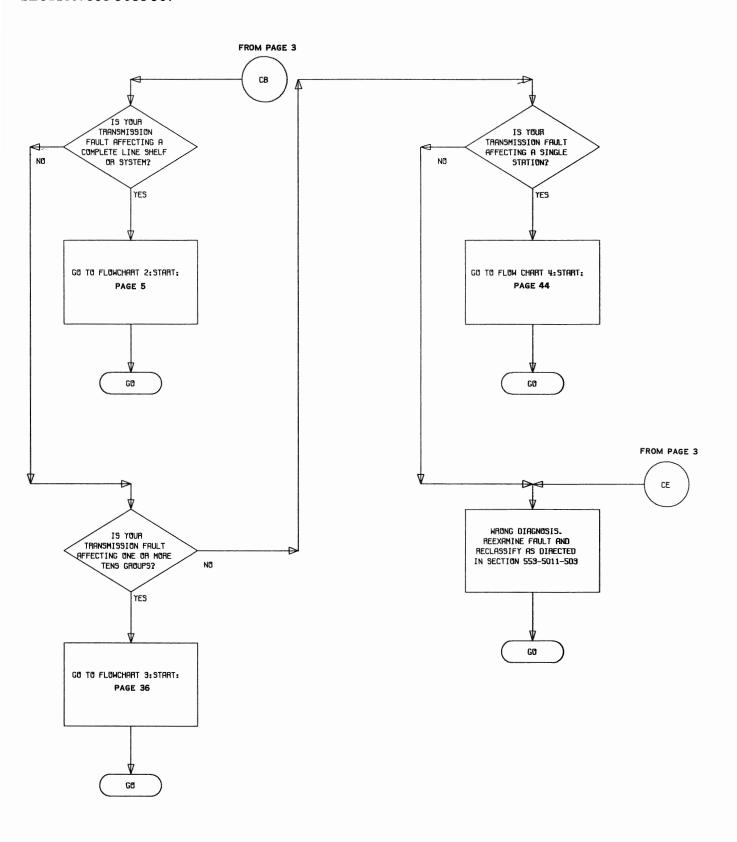
- 2.02 If a fault is cleared by circuit pack substitution and the original circuit pack has not caused a fuse to blow and/or there is no visual evidence of burnt or damaged components, the contacts on this circuit pack and its associated connector must be cleaned. The original circuit pack is then inserted in the connector and if the fault reappears the new circuit pack is reinserted.
- 2.03 If different and/or additional faults are created in the system by substituting a circuit pack, tag and return the replacement as a defective unit.
- 2.04 If the fault is not cleared by substitution of a circuit pack, the original circuit pack must be reinserted in the connector.
- 2.05 The instructions for substituting a shelf are given in Section 553-5011-202.
- 2.06 When the fault clearing procedure is completed, make a visual check to ensure that all circuit packs are well seated in their connectors and screws are tight in connector plugs and jacks. The EPABX internal cable arrangement is given in Section 553-5011-501.



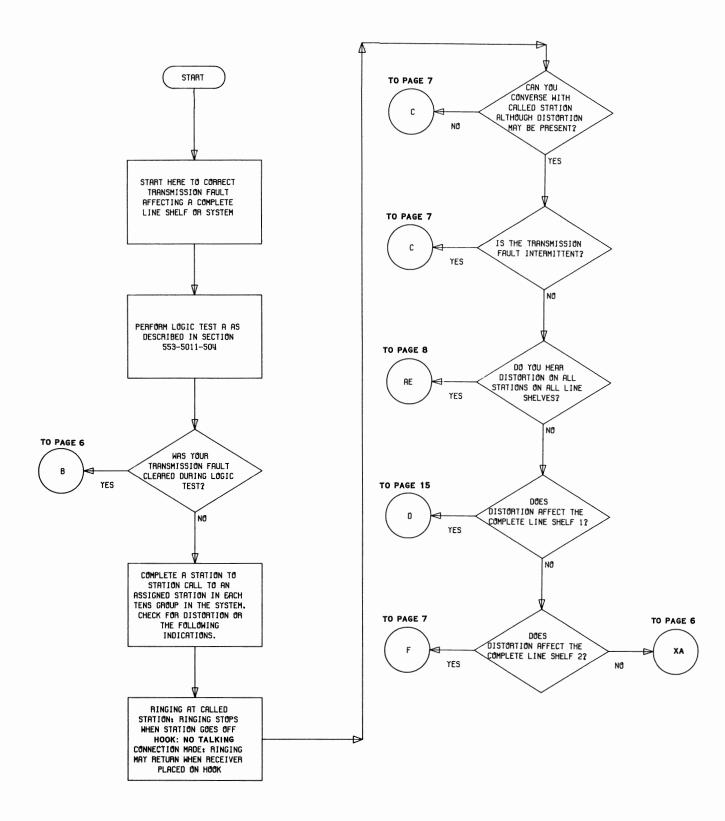
Flowchart 1 — Transmission and/or Dialing Fault Classification



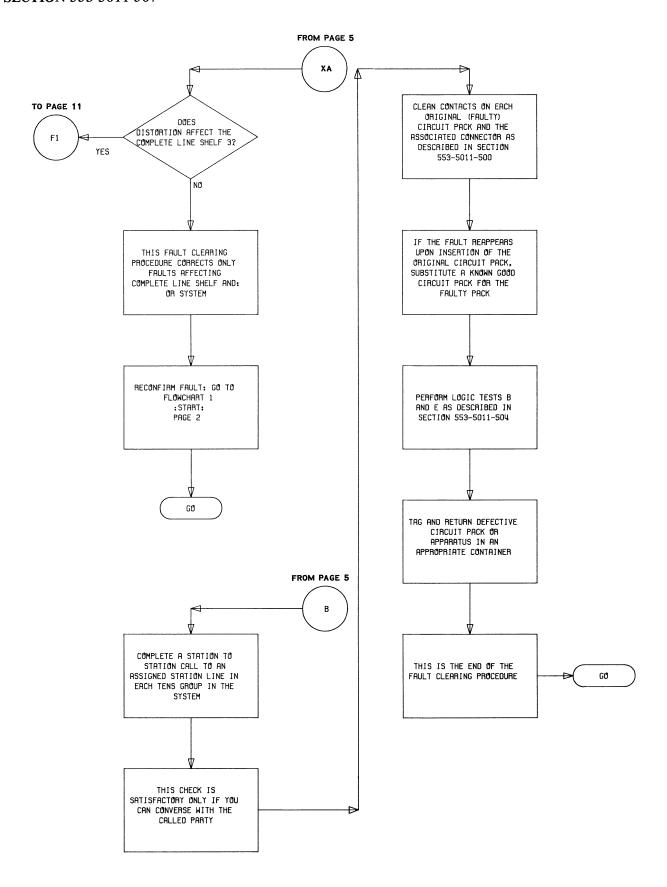
Flowchart 1 (Cont)



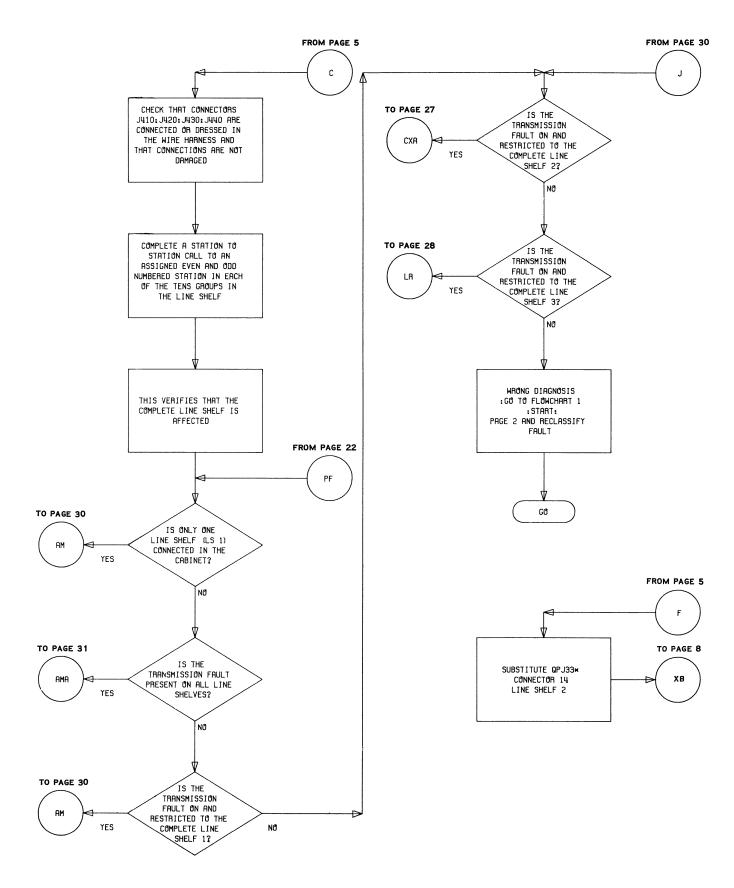
Flowchart 1 (Cont)



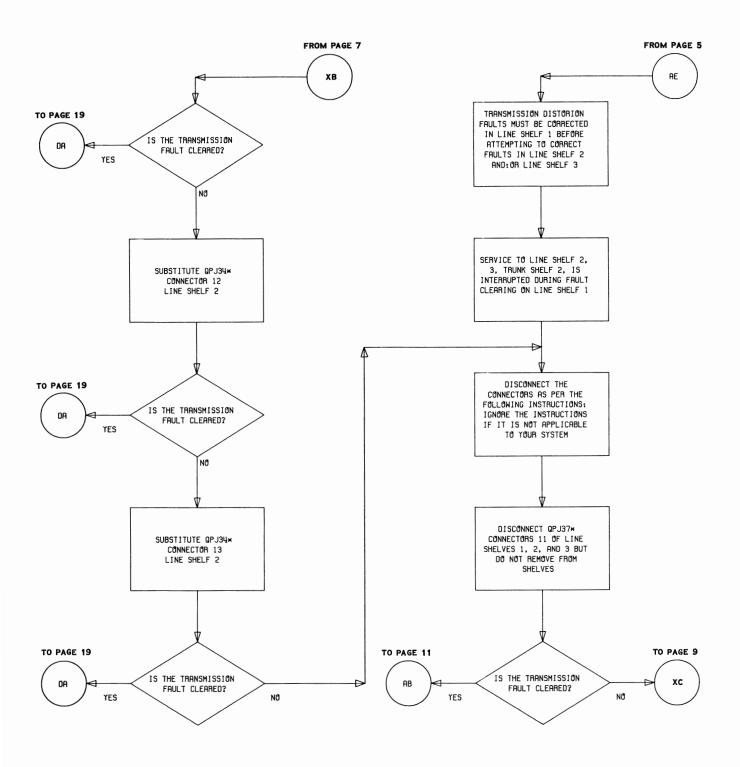
Flowchart 2 - Transmission Fault Affecting a Complete Line Shelf or System



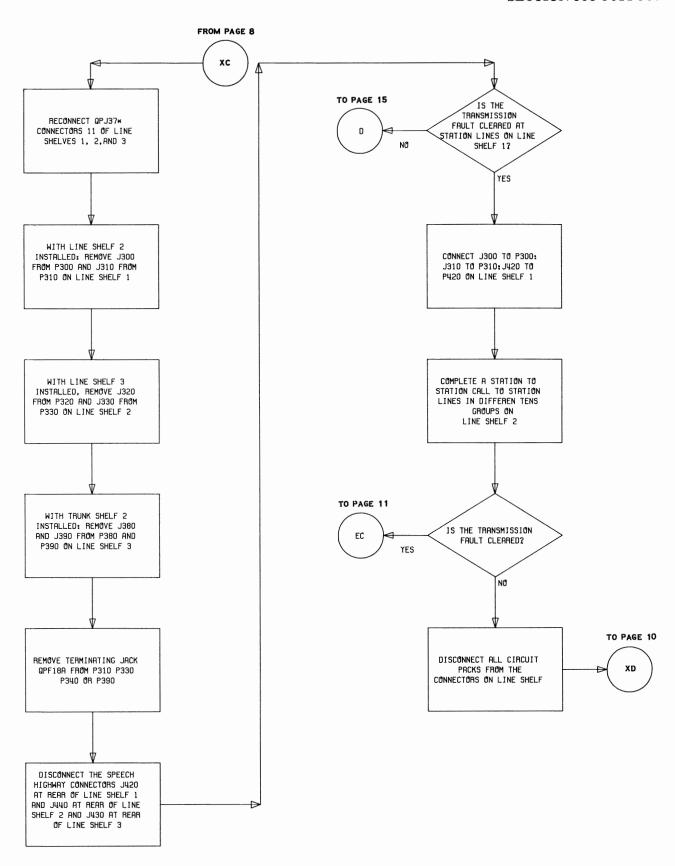
Flowchart 2 (Cont)



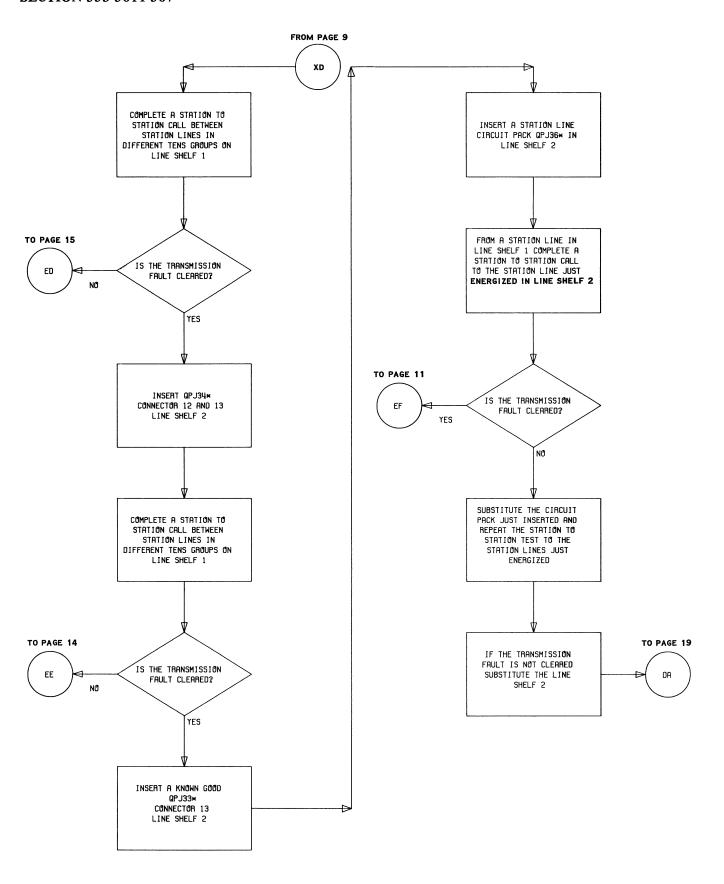
Flowchart 2 (Cont)



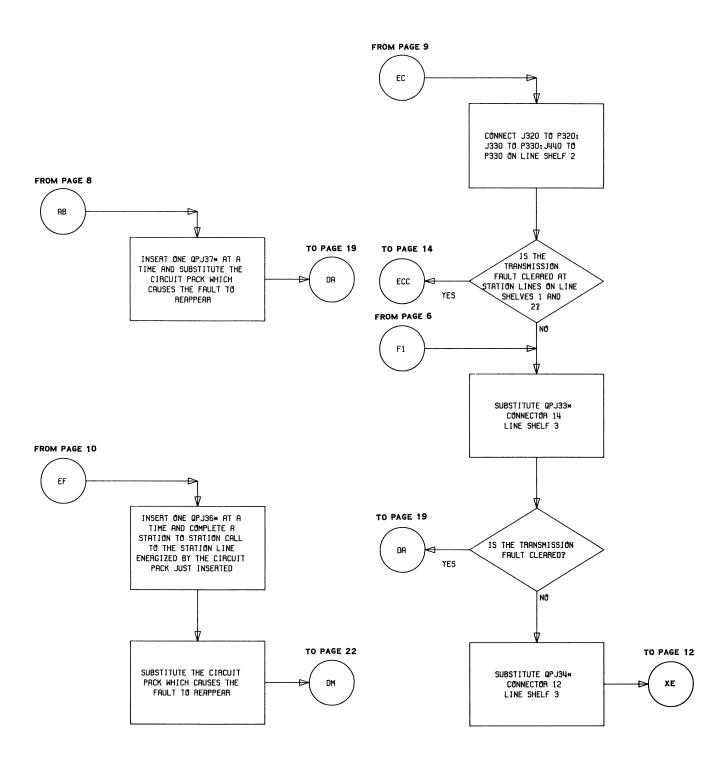
Flowchart 2 (Cont)



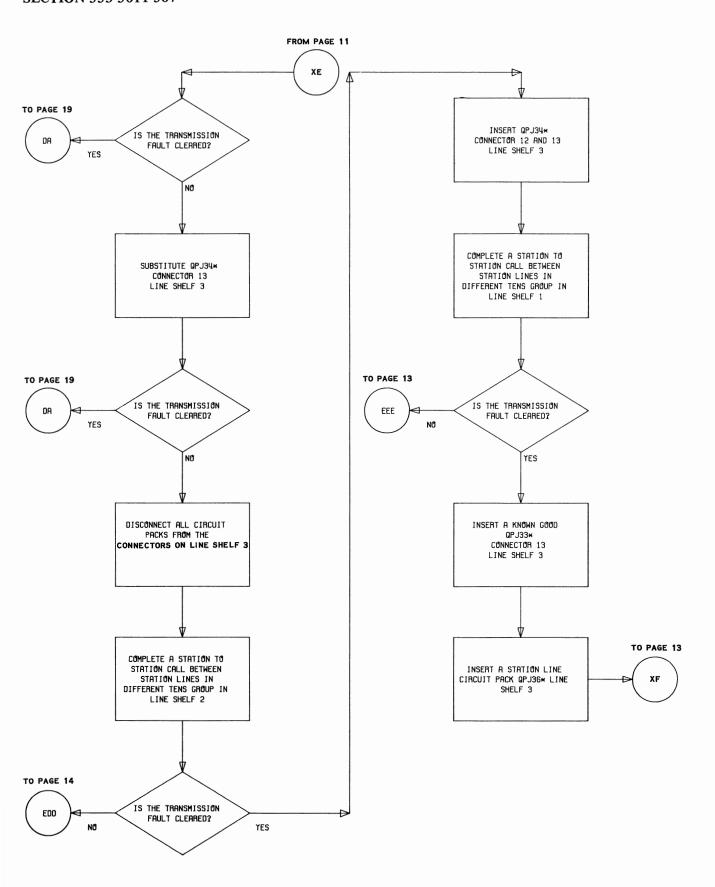
Flowchart 2 (Cont)



Flowchart 2 (Cont)

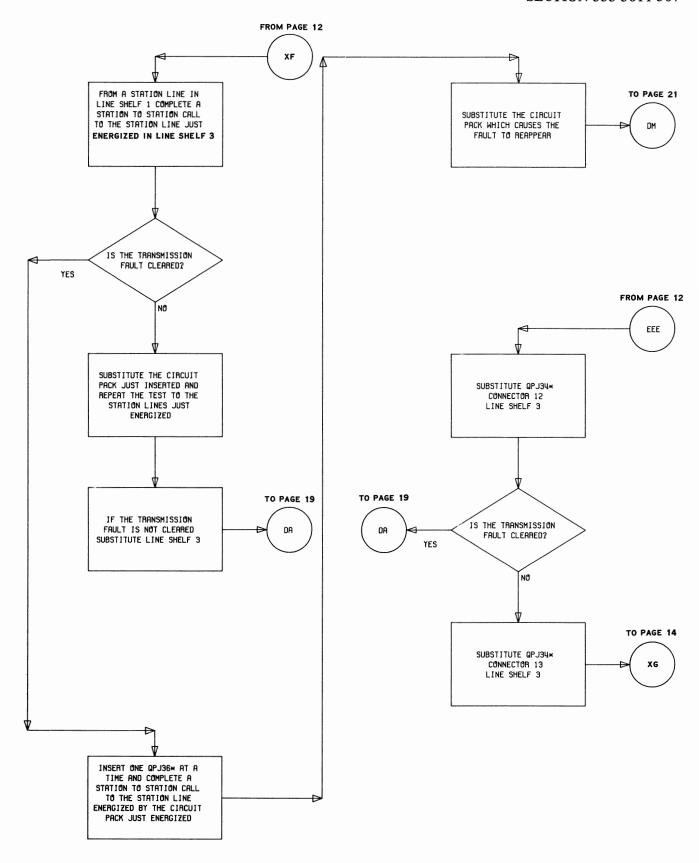


Flowchart 2 (Cont)

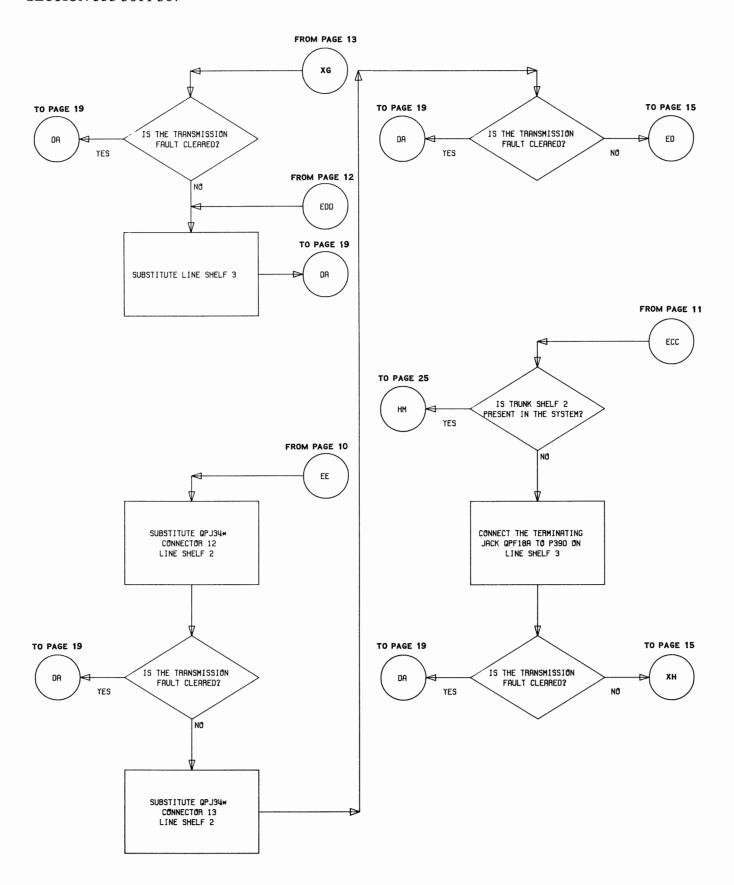


Flowchart 2 (Cont)

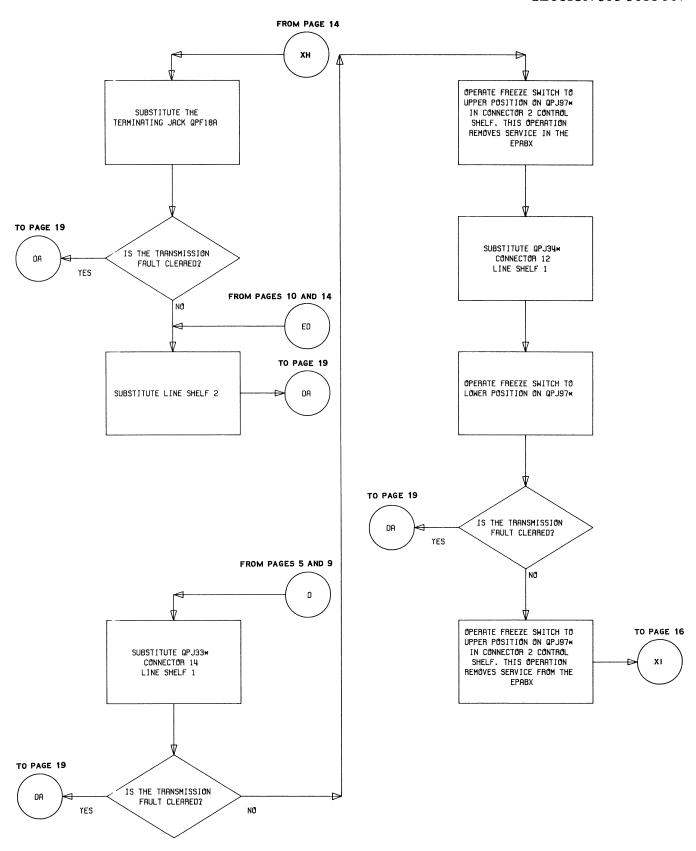
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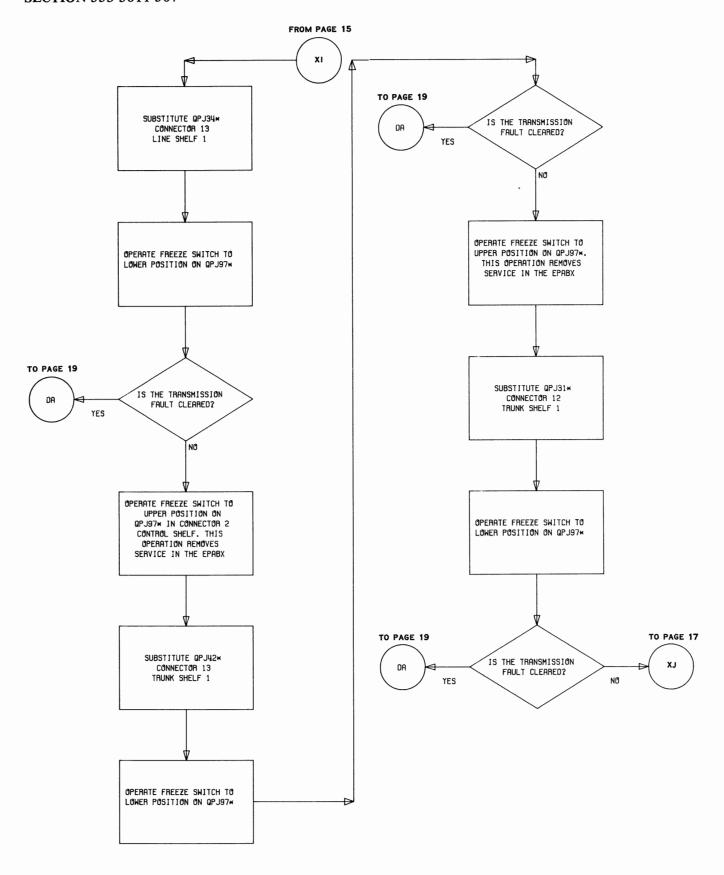
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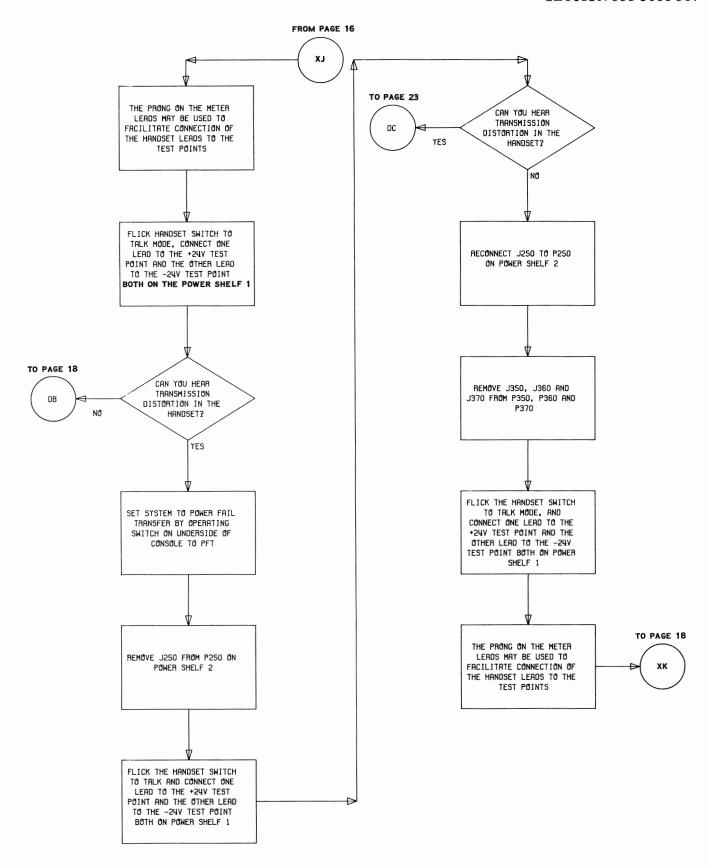
Flowchart 2 (Cont)



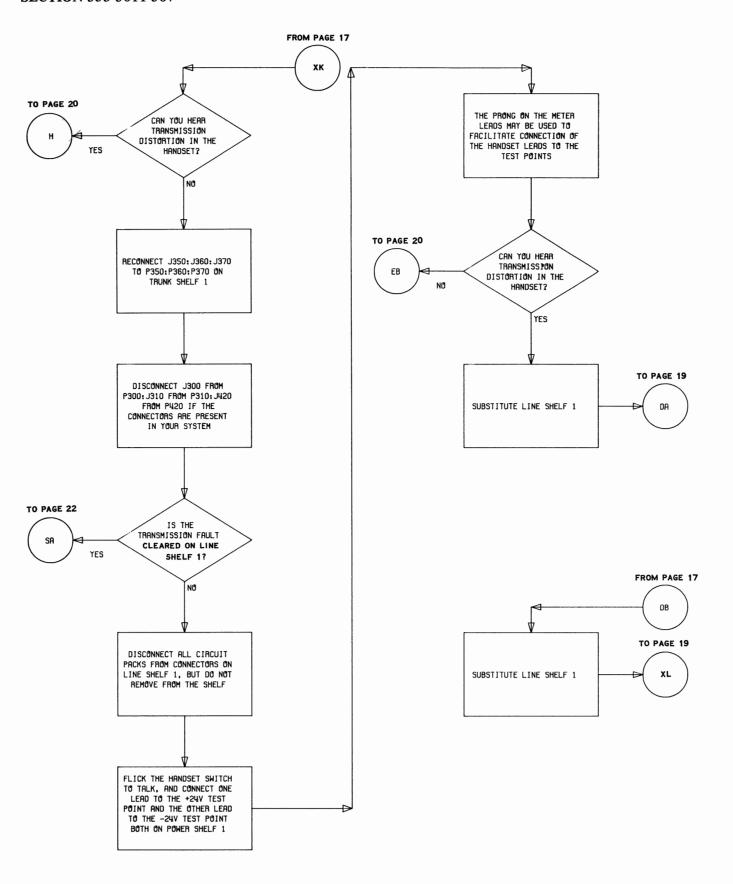
Flowchart 2 (Cont)



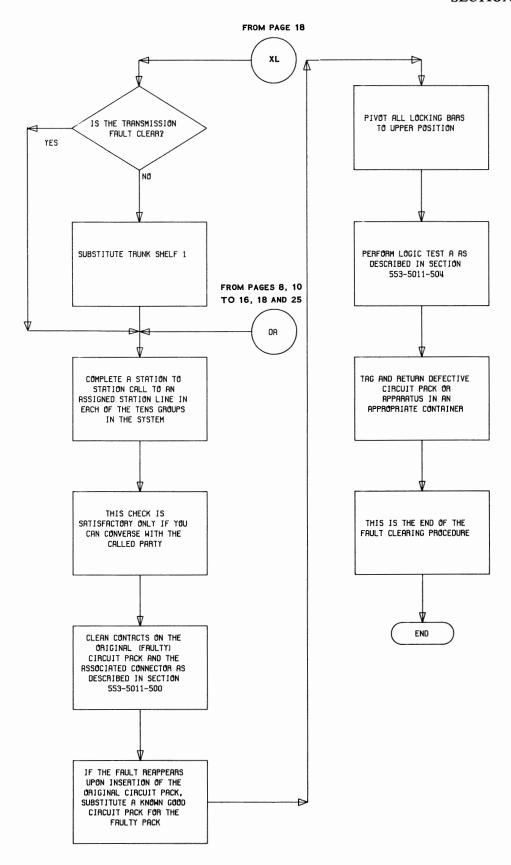
Flowchart 2 (Cont)



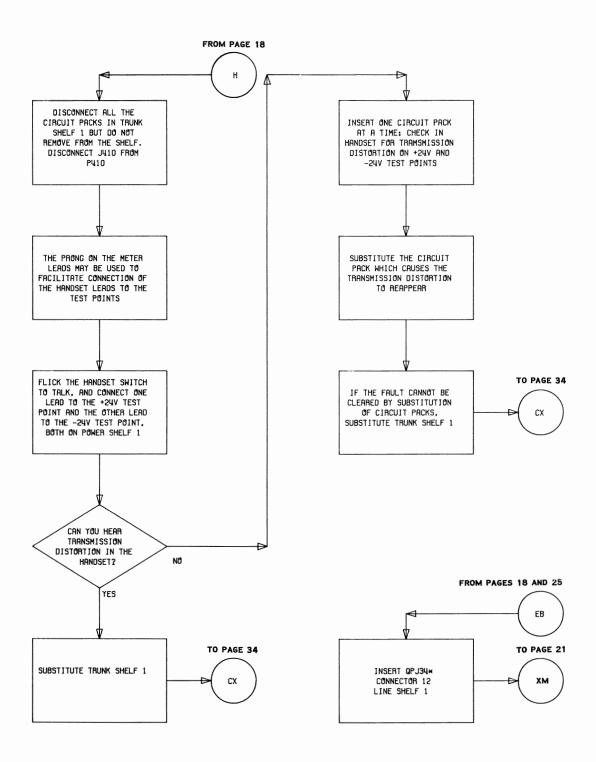
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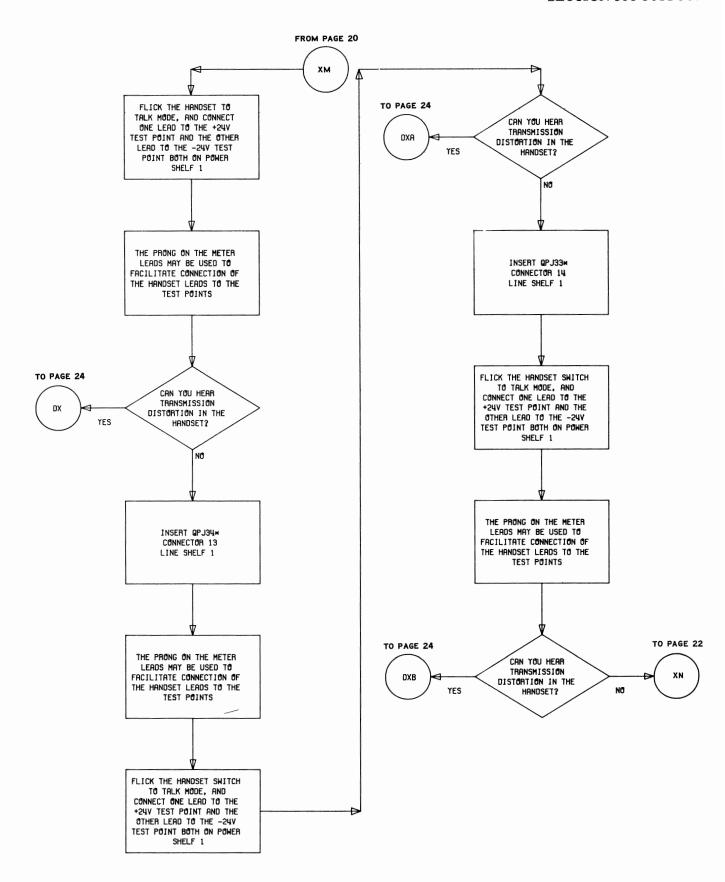
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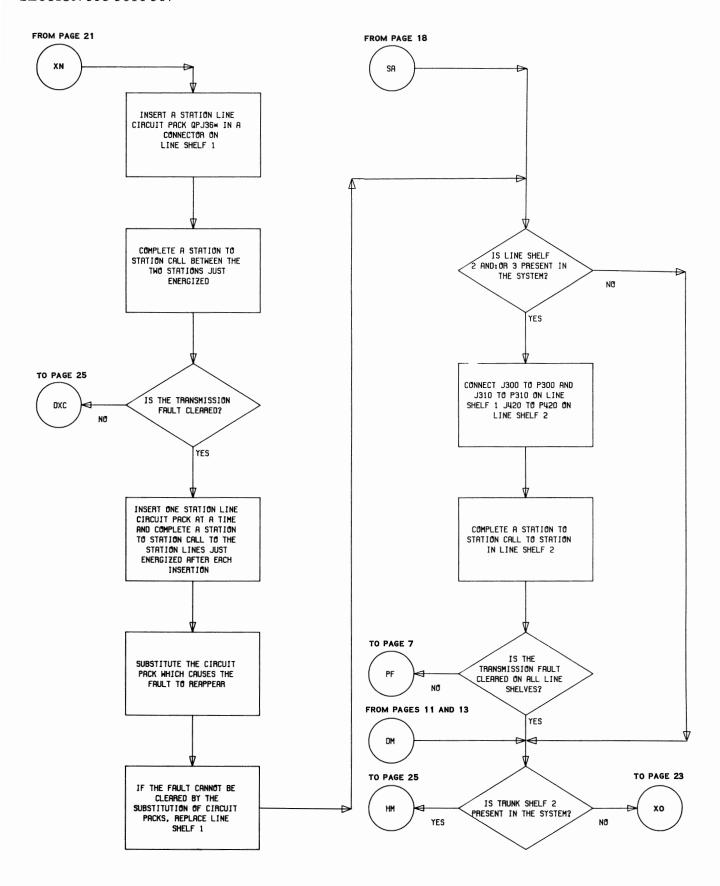
Flowchart 2 (Cont)



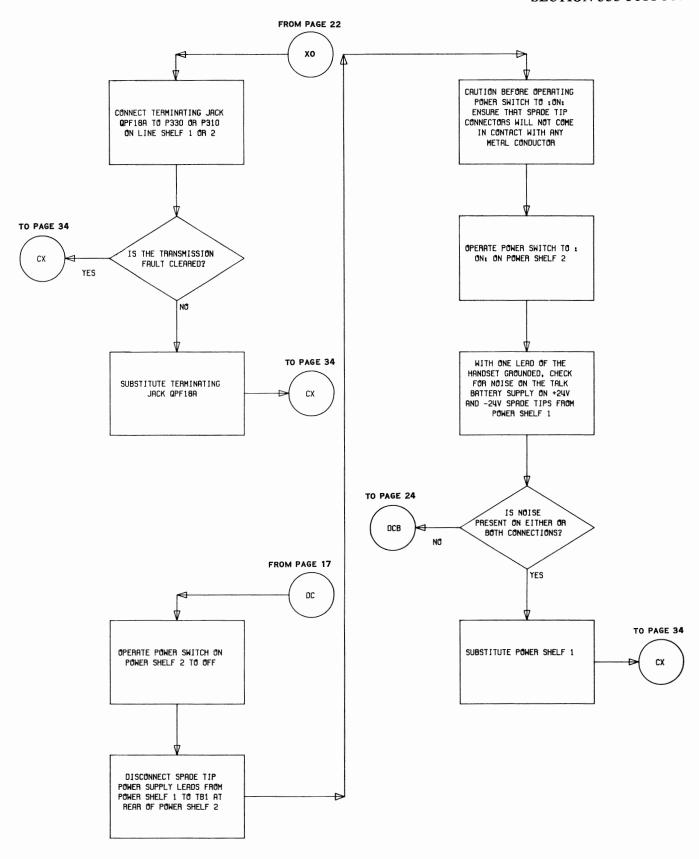
Flowchart 2 (Cont)



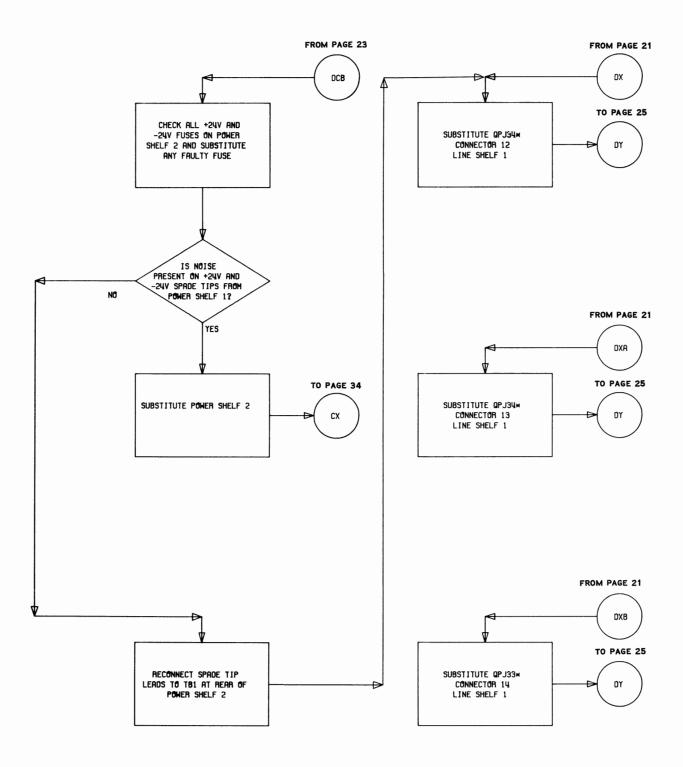
Flowchart 2 (Cont)



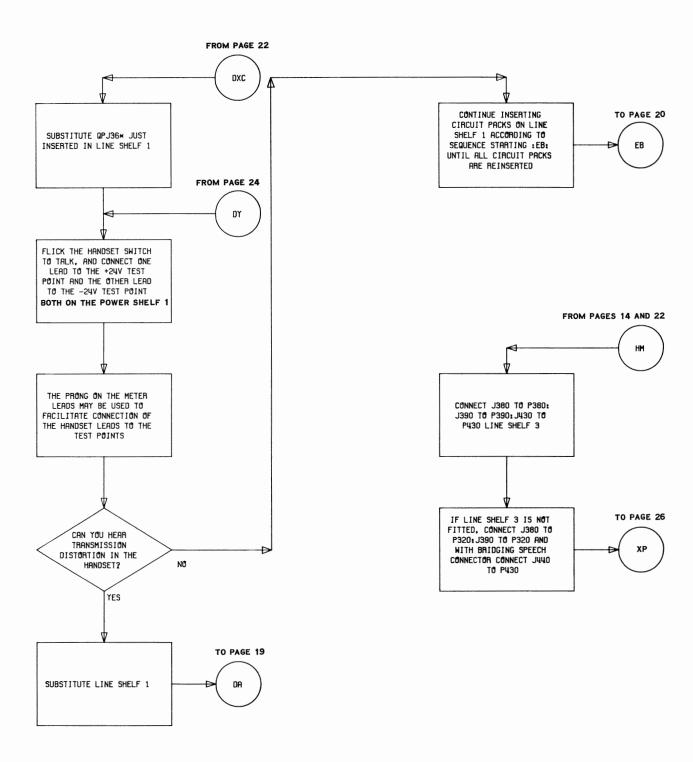
Flowchart 2 (Cont)

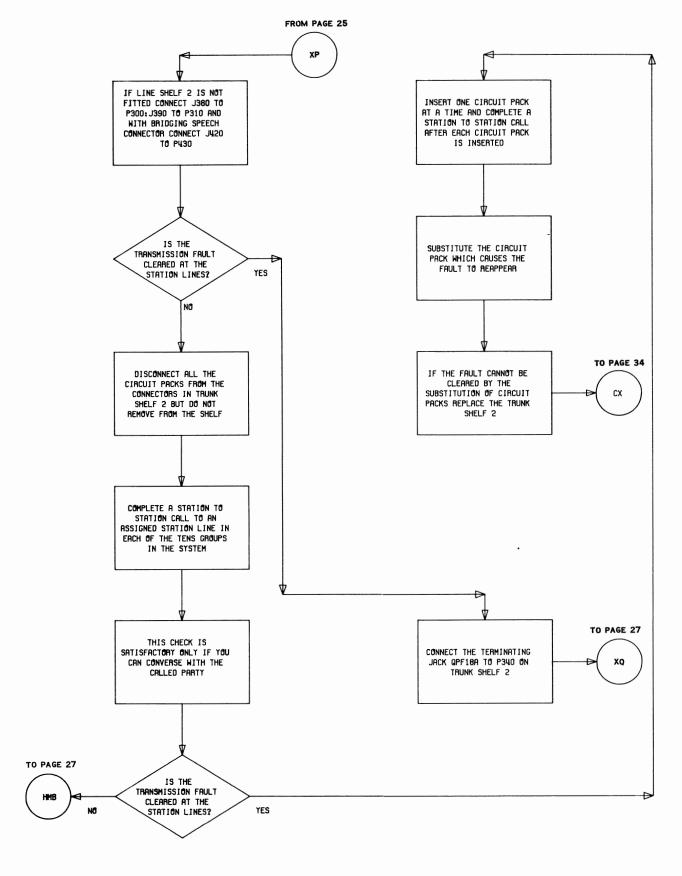


Flowchart 2 (Cont)

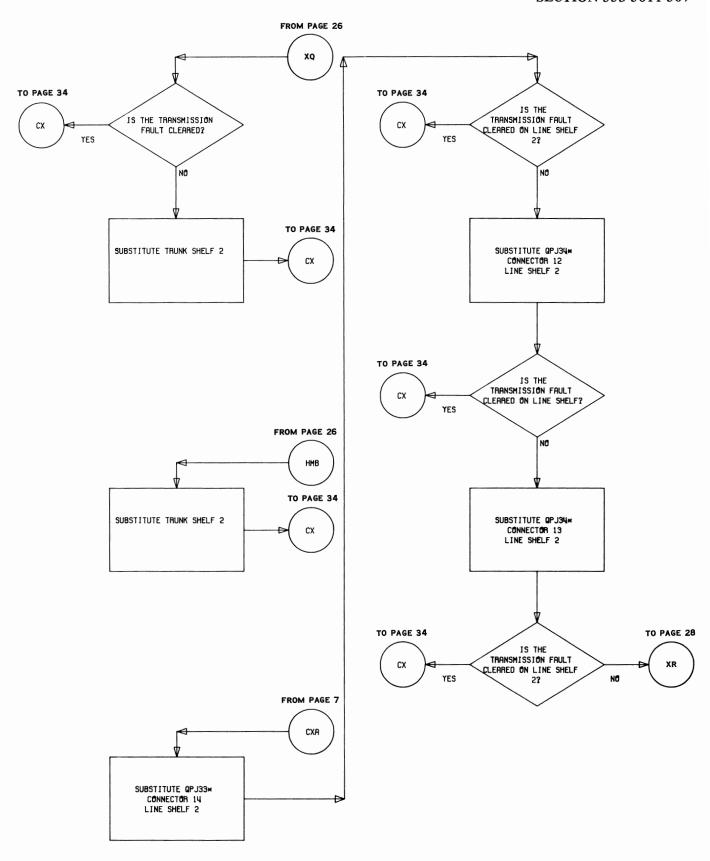


Flowchart 2 (Cont)

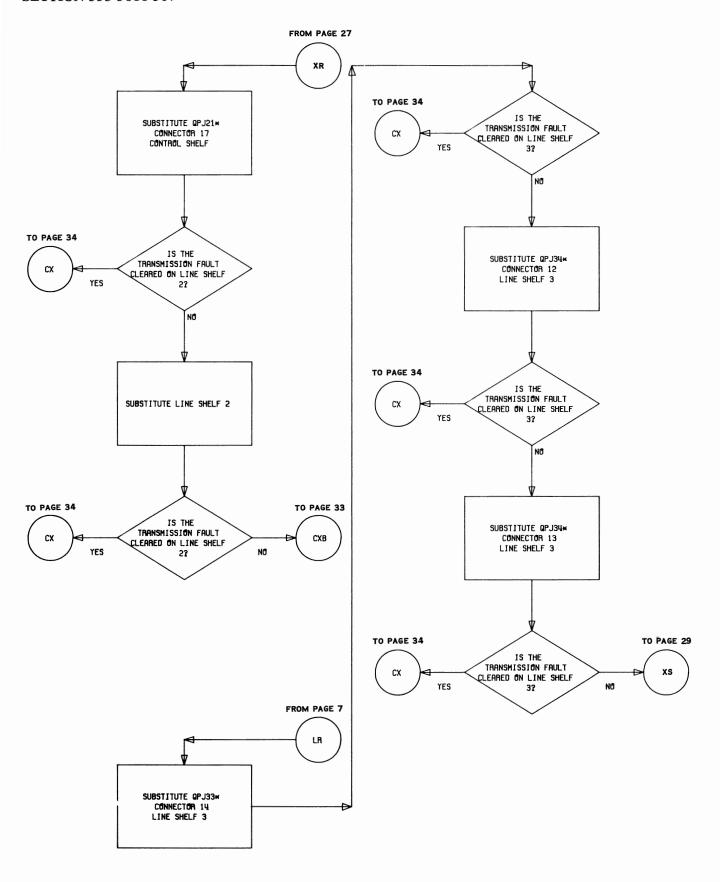




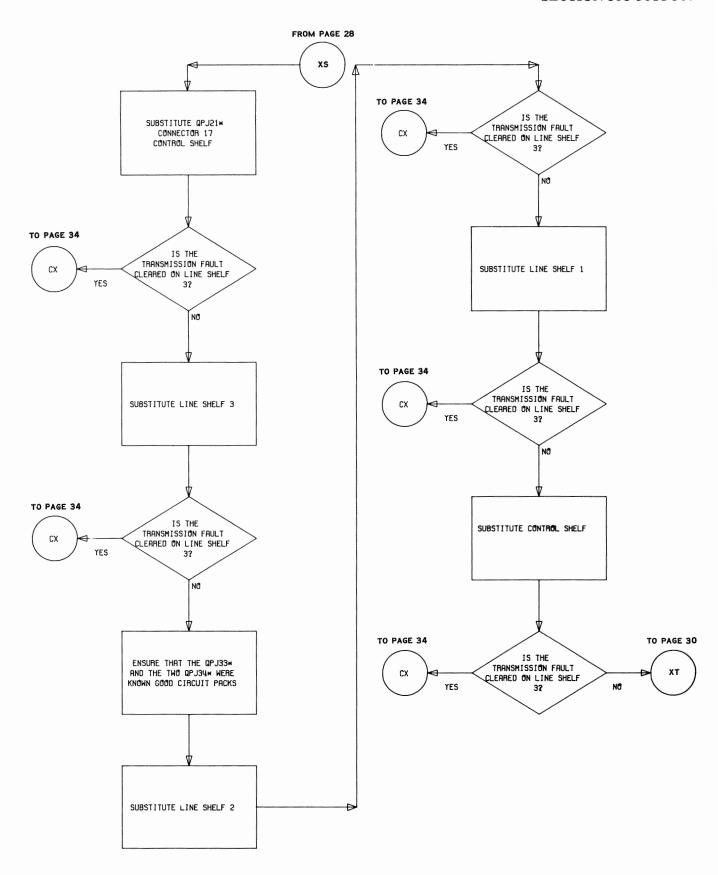
Flowchart 2 (Cont)



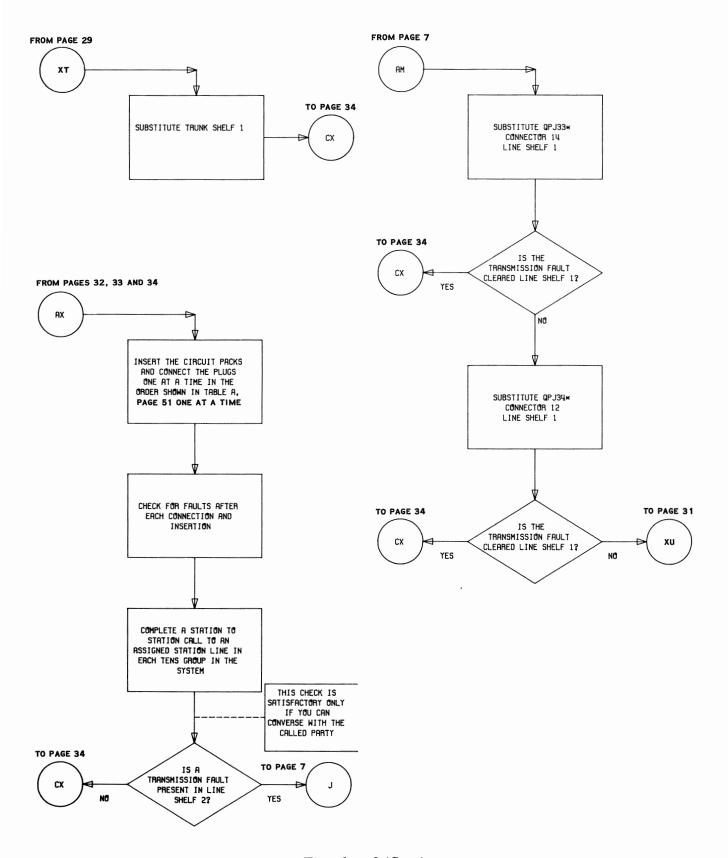
Flowchart 2 (Cont)



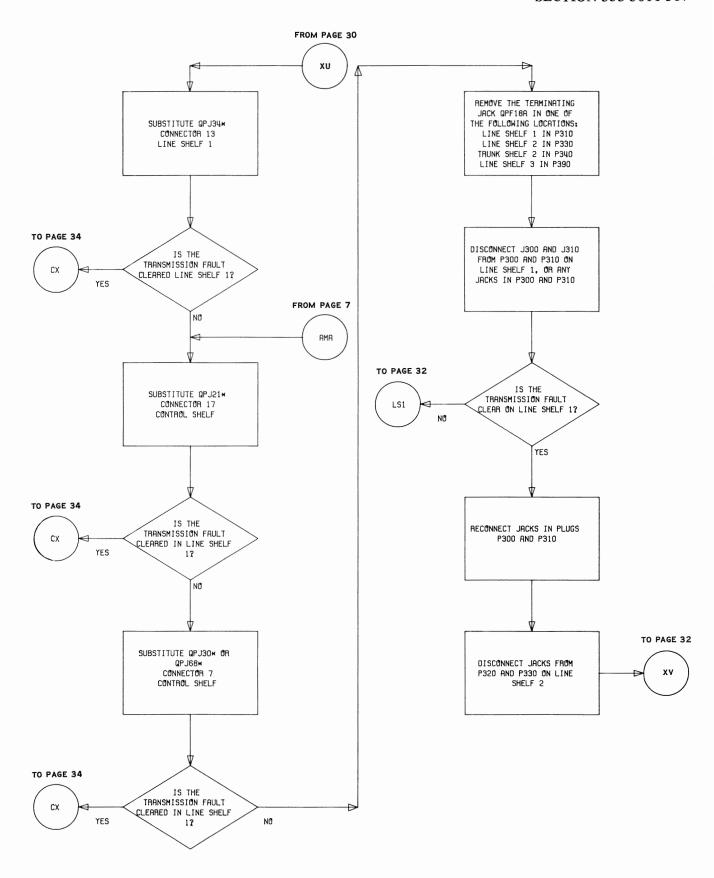
Flowchart 2 (Cont)



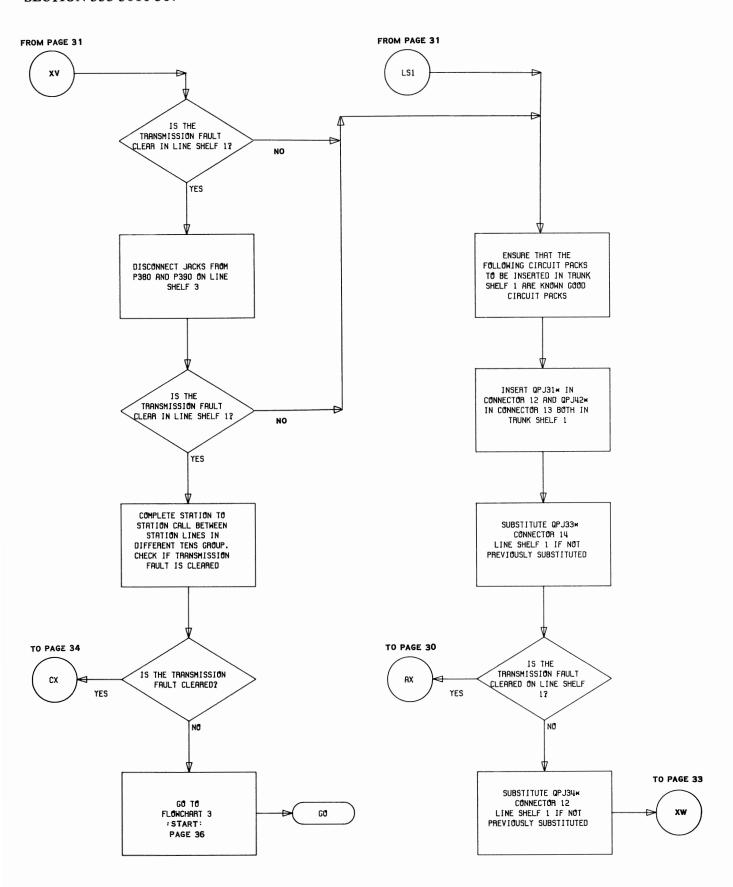
Flowchart 2 (Cont)



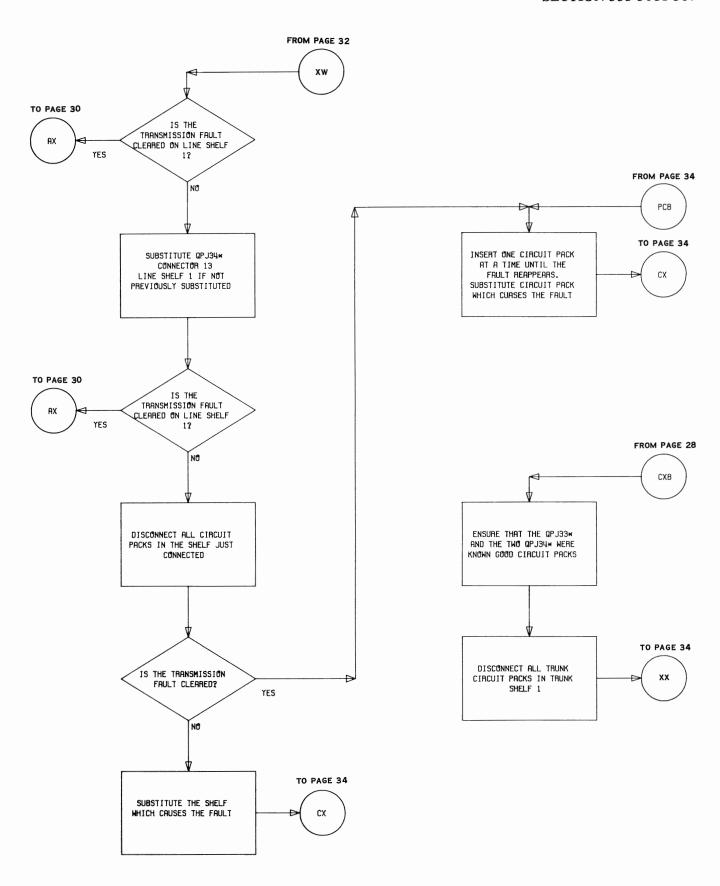
Flowchart 2 (Cont)



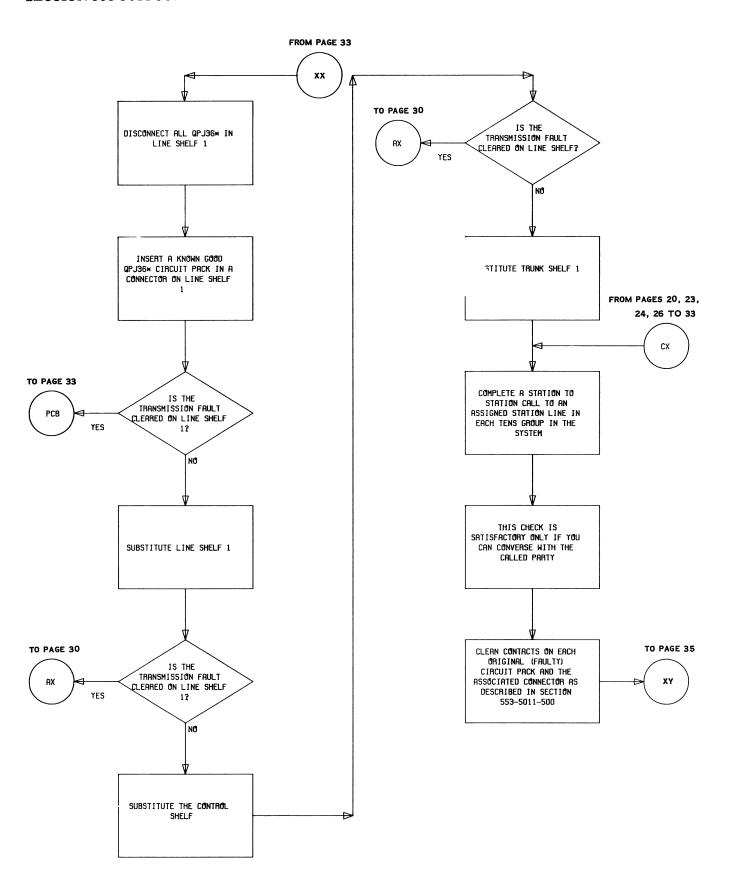
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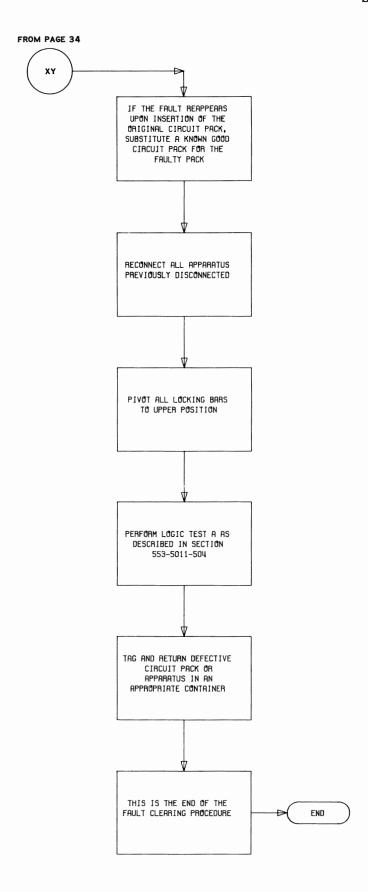
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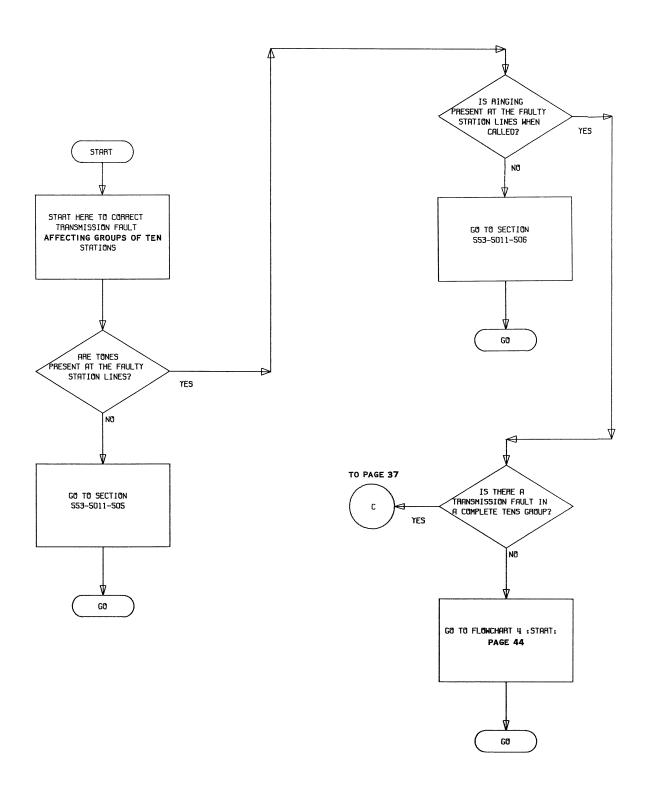
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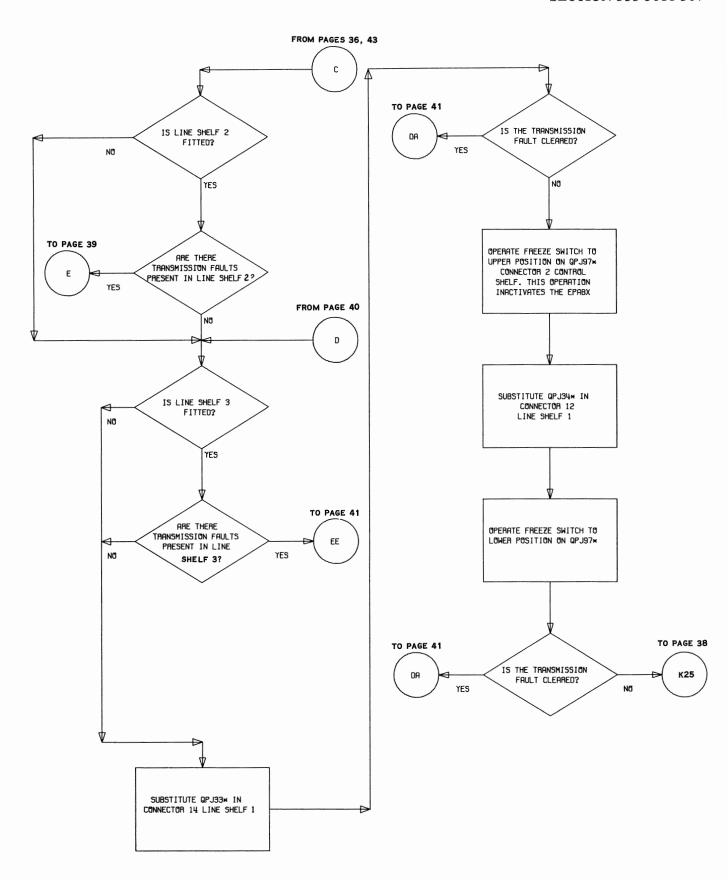
Flowchart 2 (Cont)



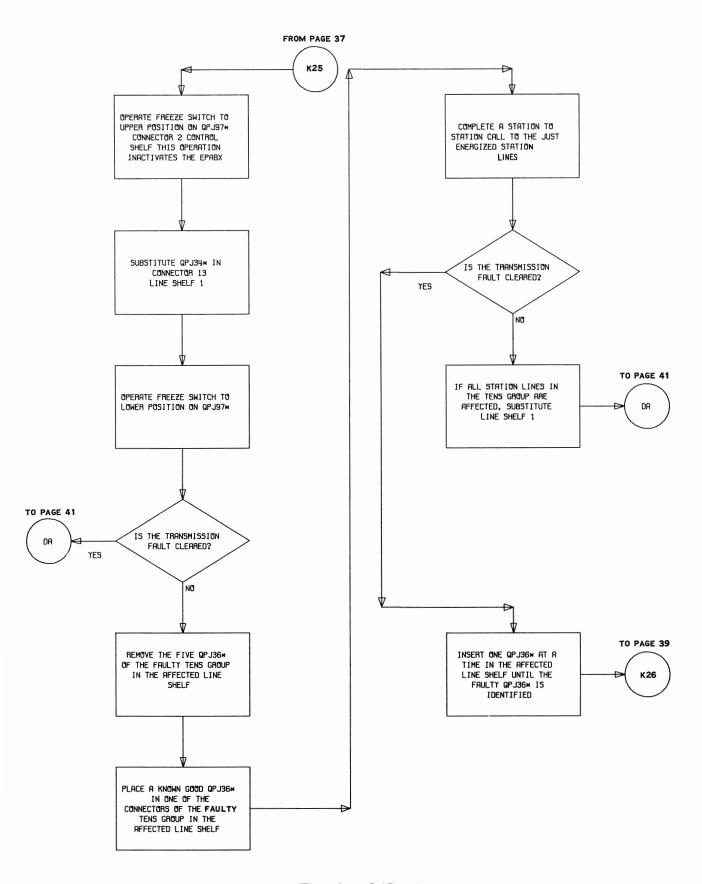
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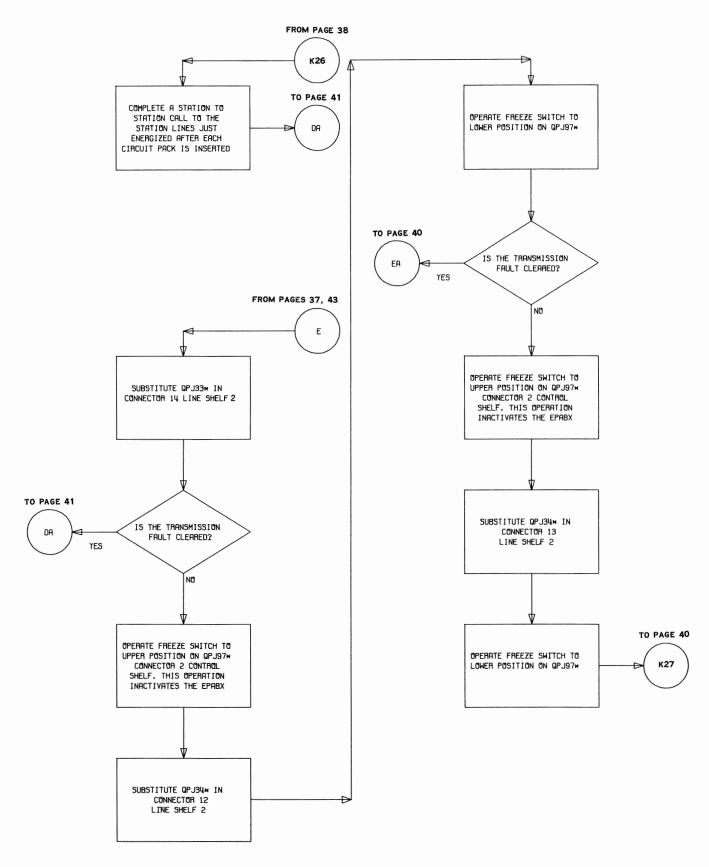
Flowchart 3 - Partial System Transmission Failure



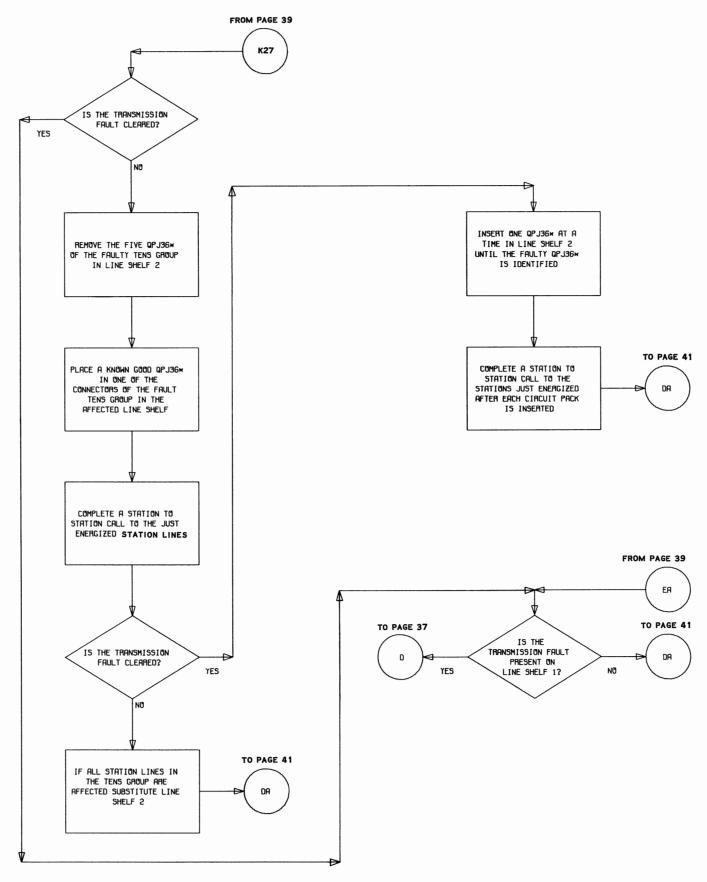
Flowchart 3 (Cont)



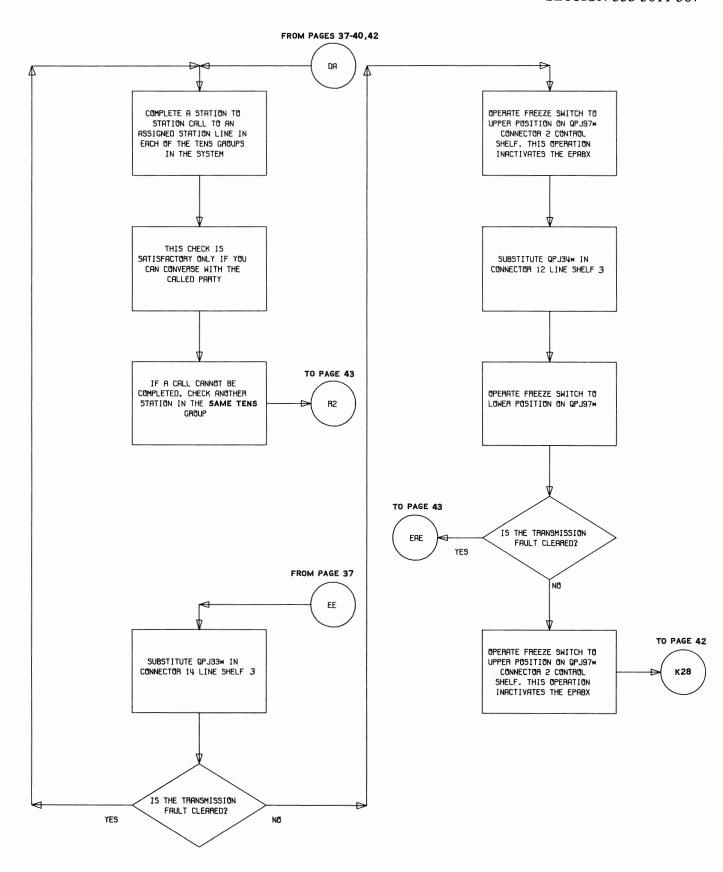
Flowchart 3 (Cont)



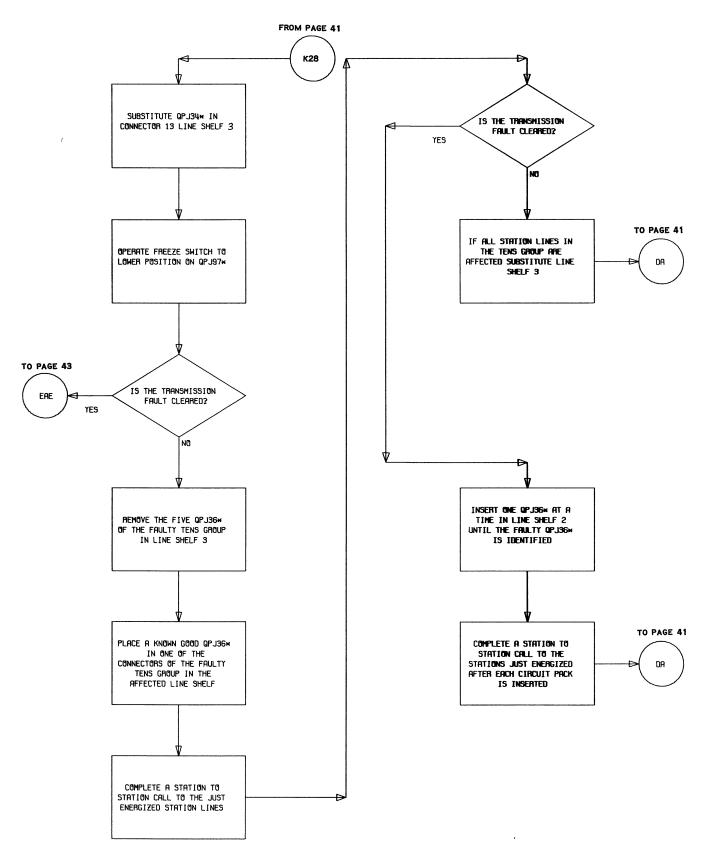
Flowchart 3 (Cont)



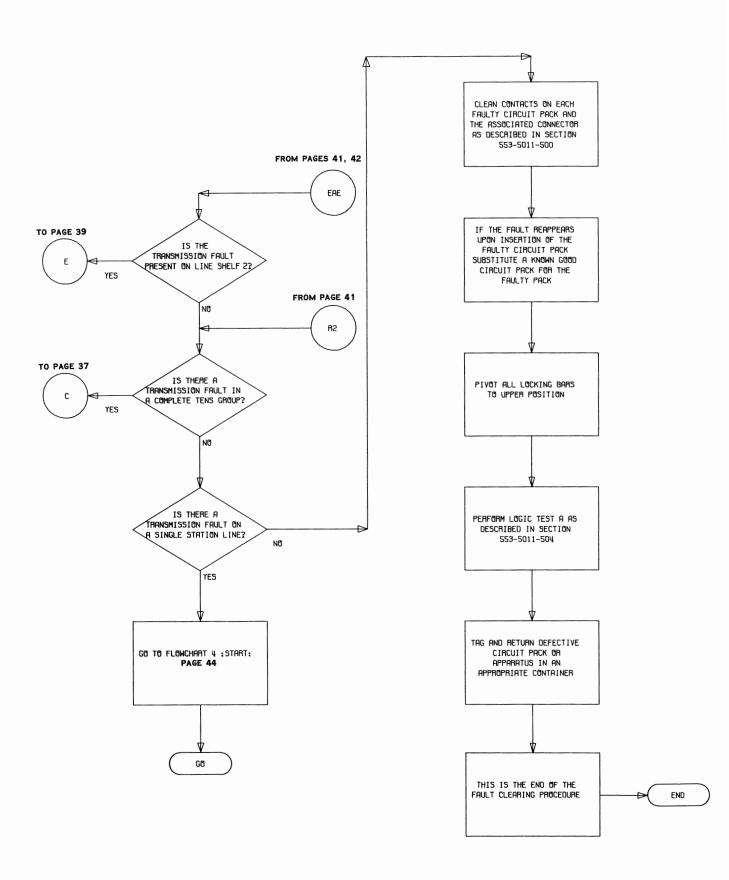
Flowchart 3 (Cont)



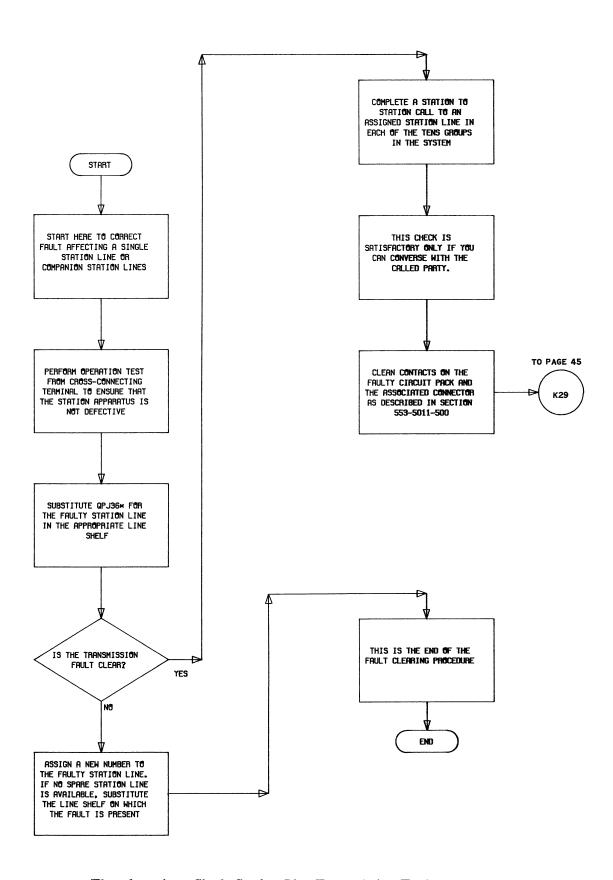
Flowchart 3 (Cont)



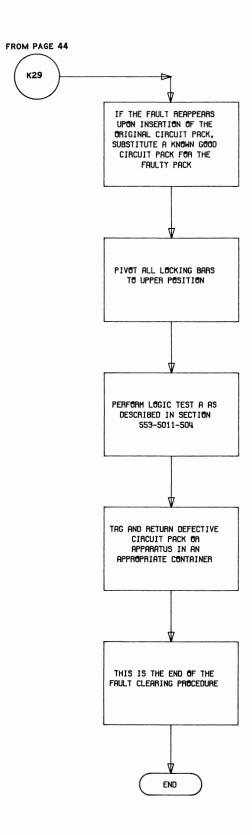
Flowchart 3 (Cont)



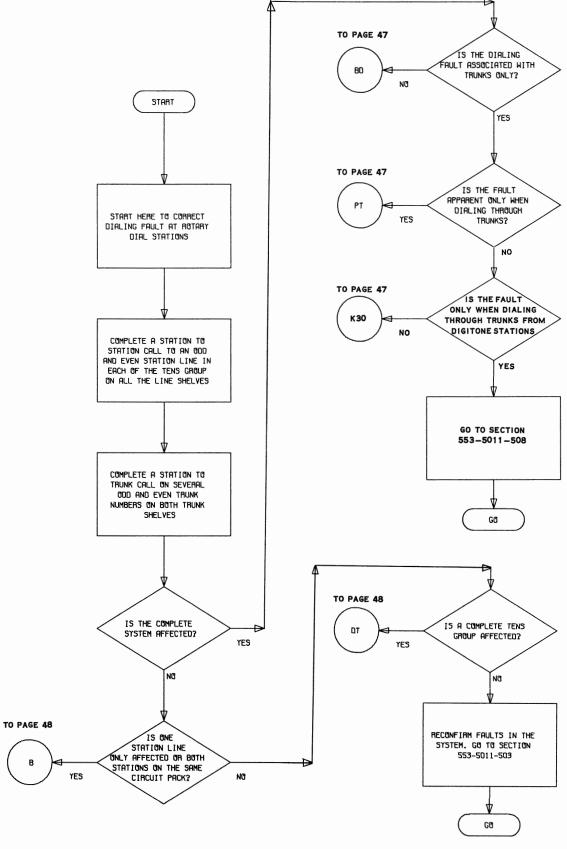
Flowchart 3 (Cont)



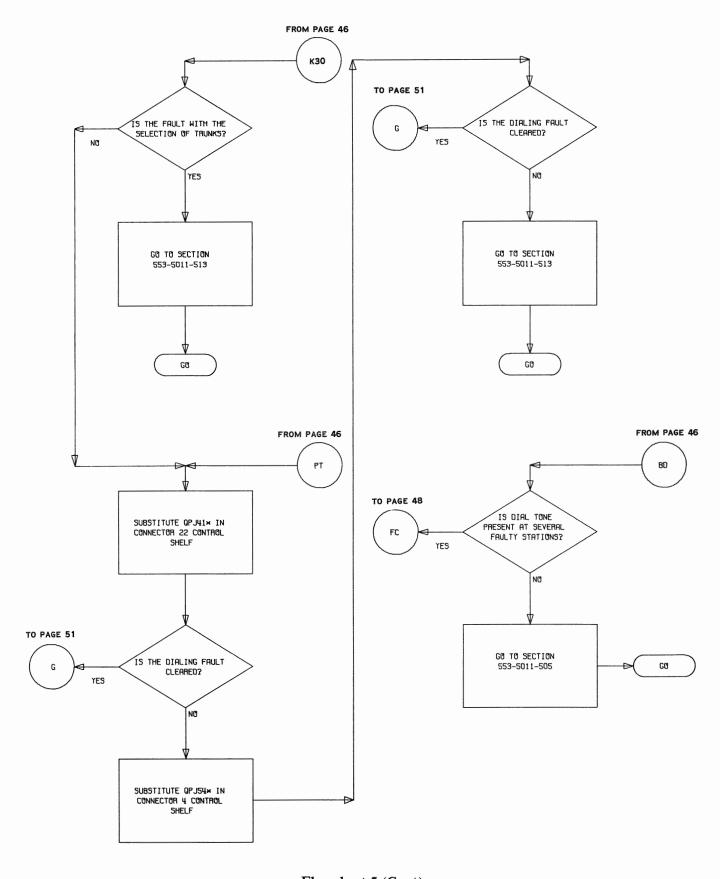
Flowchart 4 - Single Station Line Transmission Fault



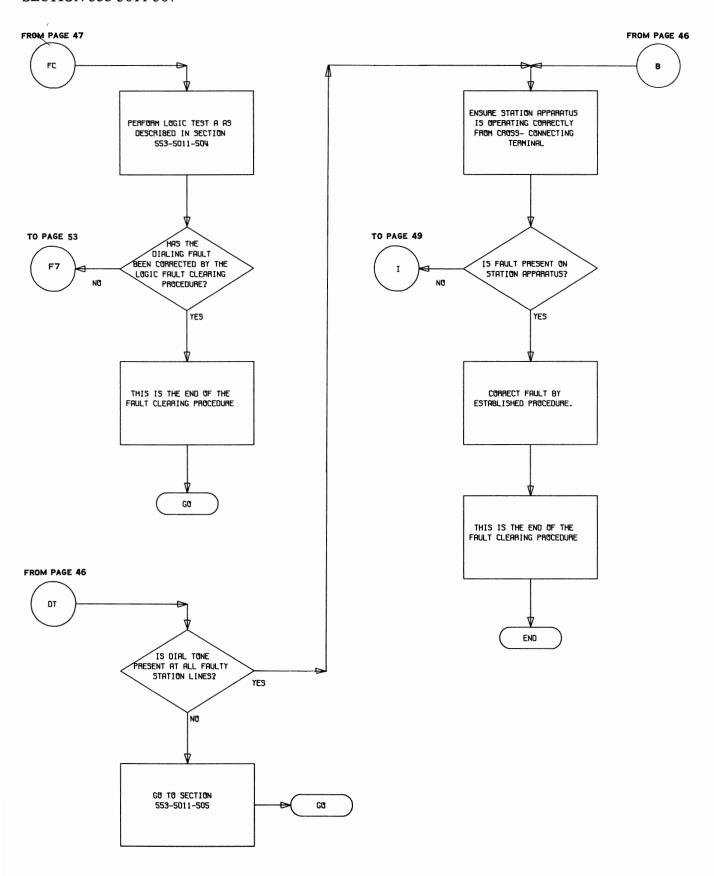
Flowchart 4 (Cont)



Flowchart 5 - Dialing Faults

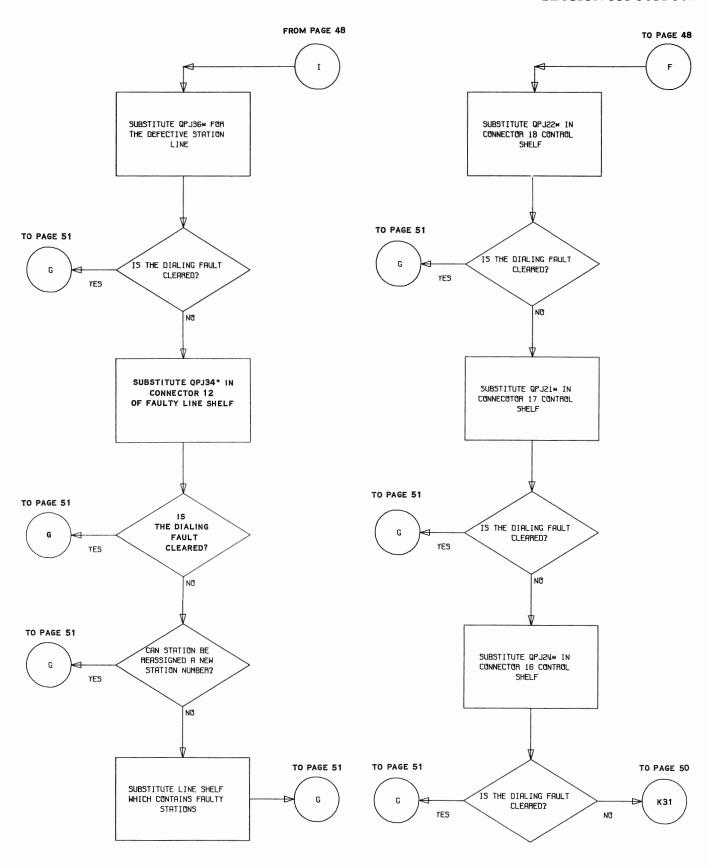


Flowchart 5 (Cont)

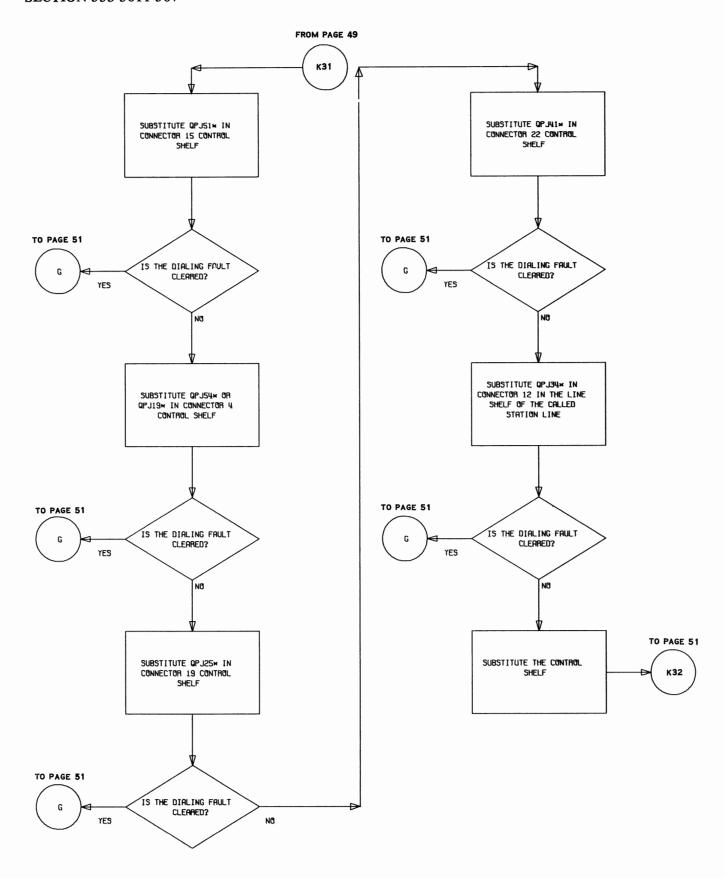


Flowchart 5 (Cont)

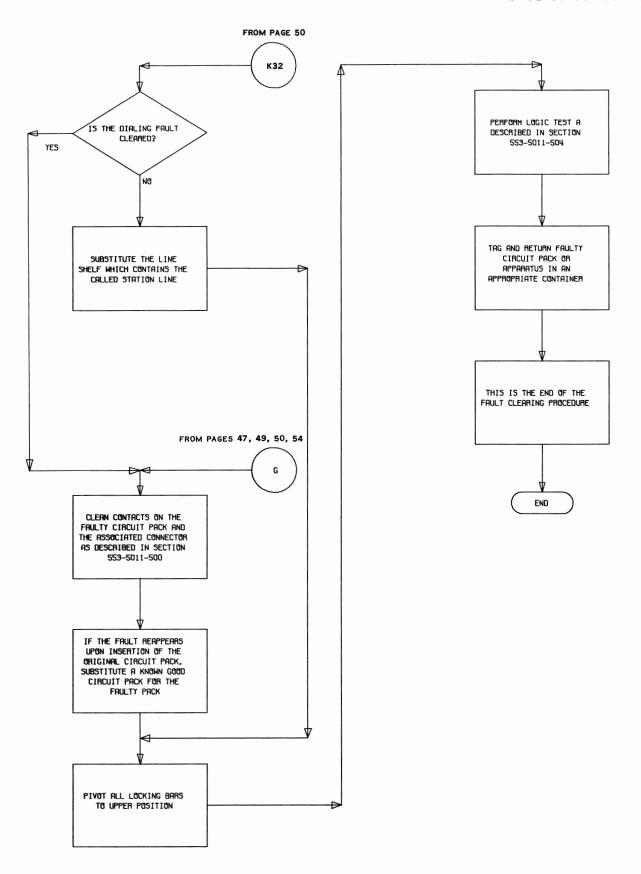
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Flowchart 5 (Cont)



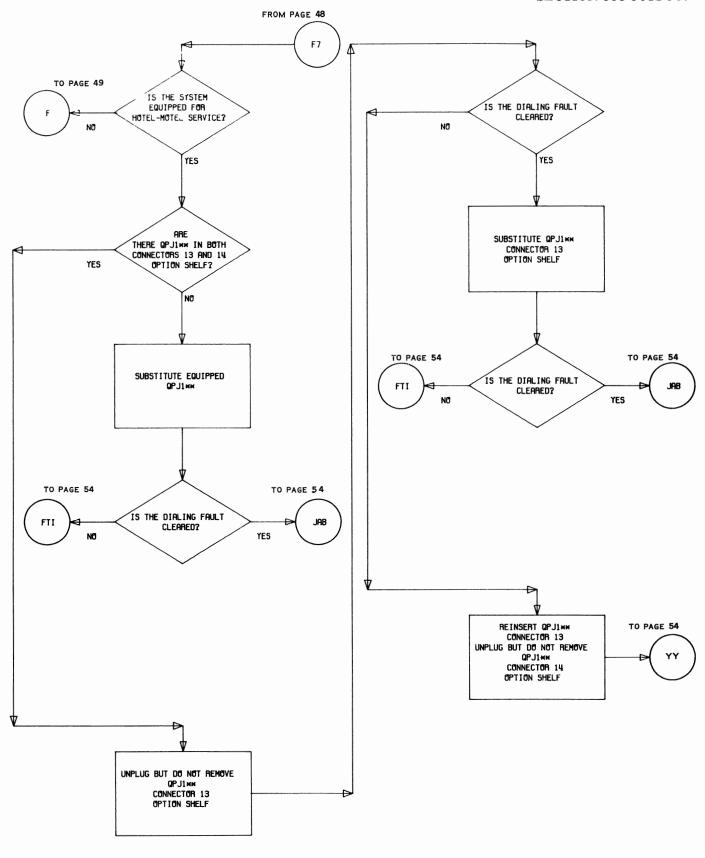
Flowchart 5 (Cont)



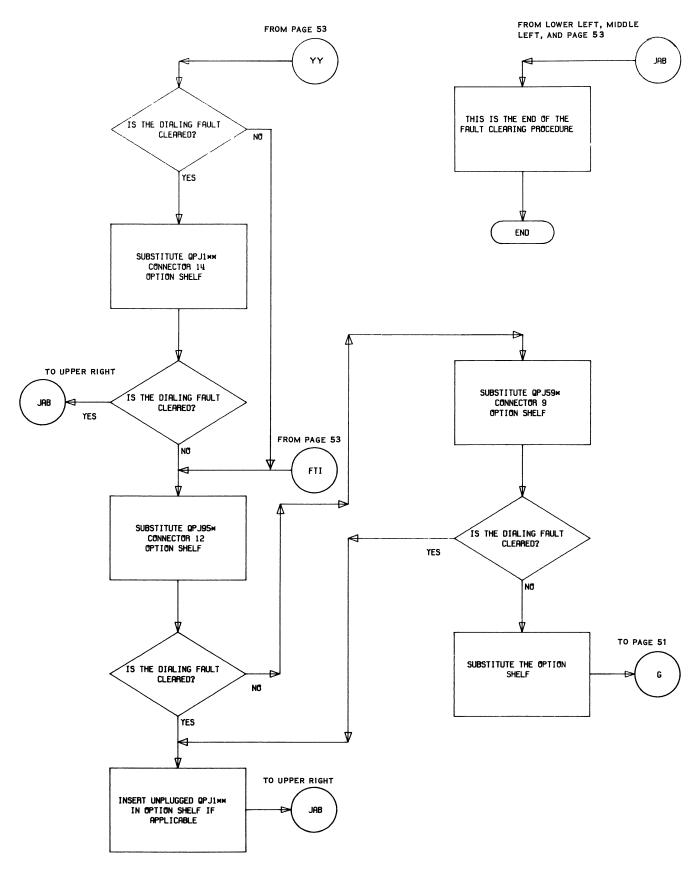
Flowchart 5 (Cont)

TABLE A SEQUENCE FOR INSERTING CIRCUIT PACKS AND JACKS

SEQUENCE	CIRCUIT PACK	CONNECTOR	JACK	SHELF
1	- - -		J350 J360 J370	Trunk no. 1 Trunk no. 1 Trunk no. 1
2	QPJ34*	12	-	Line no. 1
3	QPJ34*	13	_	Line no. 1
4	_ _	_ _	J300 J310	Line no. 1 Line no. 1
5	QPJ34*	12	_	Line no. 2
6	QPJ34*	13	_	Line no. 2
7	_ _	_ _	J320 J330	Line no. 2 Line no. 2
8	QPJ34*	12	_	Line no. 3
9	QPJ34*	13	_	Line no. 3
10	_ _	_ _	J380 J390	Line no. 3 Line no. 3
11	QPJ31*	12	_	Trunk no. 2
12	QPJ42*	13	_	Trunk no. 2
13	_	_	QPF18A	Trunk no. 2



NOTE: QPJ1\*\* MAY BE QPJ10\*, QPJ11\*, QPJ12\*, OR QPJ13\*.



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Flowchart 5 (cont)